

Subclass		CLASS		ISSUE CLASSIFICATION		P		PATENT NUMBER	
<b>U.S. UTILITY Patent Application</b>									
<i>5C3 8817</i>		<i>AK</i> 1 SCANNED		<i>O.I.P.E.</i> <i>H3</i> Q.A.		<i>1A</i>		<i>PATENT DATE</i>	
APPLICATION NO.		CONT/PRIOR		CLASS		SUBCLASS		ART UNIT	
09/941447		D		438		366		<del>223</del> <del>2812</del>	
APPLICANTS		Thomas							
Patrick Halahan Oleg Siniaguine									
TITLE		Semiconductor structures having multiple conductive layers in an opening, and methods for fabricating same							
PTO-2040 12/99									

## **ISSUING CLASSIFICATION**

<b>TERMINAL DISCLAIMER</b>		<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
		<b>Sheets Drwg.</b>	<b>Figs. Drwg.</b>	<b>Print Fig.</b>	<b>Total Claims</b>	<b>Print Claim for O.G.</b>
<input type="checkbox"/> The term of this patent subsequent to _____ (date), has been disclaimed.					<b>NOTICE OF ALLOWANCE MAILED</b>	
<input checked="" type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____		(Assistant Examiner) _____ (Date) _____			<b>ISSUE FEE</b> Amount Due _____ Date Paid _____	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.		(Primary Examiner) _____ (Date) _____			<b>ISSUE BATCH NUMBER</b> (Legal Instruments Examiner) _____ (Date) _____	

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